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Effect of *in-situ* annealed SnO₂ buffer layer on structural and electrical property of (001) SnO₂/TiO₂ heterostructures

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Abstract

We have studied heteroepitaxial growth of SnO_2 films on (001) TiO₂ substrates by pulsed laser deposition. In order to reduce crystalline defects arising from a large lattice mismatch (3.1%), SnO_2 buffer layer is employed. The buffer layer prepared by *in-situ* annealing at an optimized condition exhibits atomically flat surface and partially relaxed lattice, which play an important role in the improvement of crystallinity and electrical property for the overgrown layer. The results are discussed based on the structural characterization by means of x-ray diffraction and transmission electron microscopy and temperature dependence of Hall mobility.

1. Introduction

Transparent conducting oxides (TCO) attract increasing interests for practical application such as flat panel displays and solar cells. Recent markup of indium due to limited resources and rapidly expanding demands emerge a new technological challenge for replacing In_2O_3 -based TCO by other materials [1]. More abundant choices are ZnO and SnO₂, which can be, in principle, employed as heavily doped and degenerated semiconductors [2]. Usually, a carrier concentration of over 10^{21} cm⁻³ and a mobility of over $10 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$, yielding in a conductivity greater than 10^3 Scm^{-1} , are of interest. The carrier concentration can be controlled by dopant concentration in any form of the films from single crystalline, through texture to polycrystalline, but the mobility strongly depends on the crystallinity, although screening effect smears carrier scattering for heavily doped metallic states. For increasing the conductivity, thus it is important to elucidate the intrinsic mobility of the materials.

Among them, the intrinsic characteristics are best studied in ZnO. Hall mobility ($\mu_{\rm H}$) of 440 cm²V⁻¹s⁻¹ was demonstrated at 300 K for a single crystalline thin film with electron concentration of 9 × 10¹⁵ cm⁻³, which was grown on Mg_xZn_{1-x}O buffer layer on a lattice-matched substrates [3]. Single crystalline In₂O₃ films were prepared on nearly lattice-matched (111) YSZ substrate to record $\mu_{\rm H}$ of 110 cm²V⁻¹s⁻¹ at 300 K for a film with electron concentration of 7 × 10¹⁸ cm⁻³ [4]. These high-quality epilayers usually show a bell-shaped temperature dependence of $\mu_{\rm H}$, where the mobility first increases with lowering temperature due to suppression of phonon scattering and then declines due to impurity scattering as the carriers rapidly freeze out.

A bulk SnO₂ single crystal has the highest mobility among rutile-type oxide ($\mu_{\rm H} = 240 \text{ cm}^2 \text{V}^{-1} \text{s}^{-1}$ at 300 K) [5]. There have been reported many studies of SnO₂ texture films grown on such substrates as *r*-plane sapphire [6] and (100) TiO₂ [7]. However, temperature dependence of the electrical properties has not yet been investigated. Therefore, it is interesting to examine the electrical properties for an epitaxially grown SnO₂ film.

In this study, we employ (001) rutile TiO₂ substrates. Its crystal structure is identical to that of SnO₂ and the lattice mismatch is 3.1% along the [100] direction and 7.7% along the [001] direction, respectively. In order to minimize the influence of lattice mismatch, we show that the use of *in-situ* annealed SnO₂ buffer layer is an effective way of reducing the defect extending from the substrate/film interface to the SnO₂ layer, yielding in a low-temperature $\mu_{\rm H}$ substantially higher than that of as-grown films.

2. Experimental

The films were deposited on (001) TiO₂ substrates by pulsed laser deposition. A SnO₂ ceramic target (99.99% purity), placed 45 mm away from the substrate surface, was ablated with KrF excimer laser pulses at a repetition of 10 Hz and at a fluence of 1.5 J/cm². The substrates were etched in 23% HF aqueous solution and annealed at 1100°C for 12 hours in air prior to the film deposition in order to prepare atomically flat surface [8]. Approximately 100 nm-thick films were deposited (hereafter denoted as as-grown) at 540°C and at an oxygen pressure (P_{O2}) of 1 × 10⁻³ Torr. Some of the as-grown films were annealed at 750°C or 800°C in $P_{O2} = 1 \times 10^{-8}$ Torr for 1 hour and then annealed in 1 atm of oxygen gas at 400°C in the deposition chamber (denoted as 750°C-annealed and 800°C-annealed, respectively). The overgrowth layers of SnO₂ were prepared on the 750°C-annealed films with a thickness of 30 nm (denoted as overgrown).

The crystallinity of the films was characterized by a powder x-ray diffraction (XRD) apparatus (Rigaku, RINT-RAPID) and a high-resolution four-circle XRD apparatus (Philips, X'-pert MRD). The film surface was characterized by reflection high-energy electron diffraction (RHEED) and atomic force microscopy (AFM). The electrical properties were measured by Quantum Design PPMS. Cross-sectional transmission electron microscopy (TEM, JEM-2000EX2 operated at 200 kV) observation was also carried out to characterize defect structure in the heterostructures.

3. Results and discussion

We first optimized the deposition conditions of SnO_2 film by varying growth temperature from 450°C to 650°C and P_{02} from 1 ×10⁻⁶ Torr to 1 ×10⁻² Torr. Except for the films grown in 1 ×10⁻³ Torr, RHEED patterns showed 3D-growth mode and the AFM image indicated rough surface. For the films grown in 1 $\times 10^{-3}$ Torr, a weak streak RHEED pattern and smooth surface were obtained at growth temperature of 540°C, although step and terrace structures were not clearly observed in the AFM image [Figure 1 (a) and (e)]. Next, we carefully sought an optimal annealing condition for preparing excellent buffer layer. In order to improve surface flatness, we first rose temperature up to 800°C while maintaining P_{02} set at growth condition, *i.e.*, 1 mTorr, but RHEED pattern remained unchanged. By decreasing P_{O2} to 1×10^{-8} Torr, the surface became much smoother. The 750°C-annealed film showed a fine streak RHEED pattern [Figure 1(b)] and step and terrace structures in AFM image [Figure 1(f)]. However, the 800°C-annealed film had apparently rough surface as shown in the AFM image [Figure 1(g)] and spotty RHEED pattern [Figure 1(c)]. Therefore, an optimal annealing condition for the buffer layer was set as 750° C in $P_{O2} = 1$ $\times 10^{-8}$ Torr. Indeed, the atomically flat surface was kept after overgrowth of SnO₂ film on the buffer layer [9] and ZnO [10] heteroepitaxial films. Another annealing step in 1 atm of oxygen was followed in order to annihilate oxygen vacancies in the film and substrates.

Figure 2(a) shows XRD pattern of the as-grown film. The strong (002) peak indicates that the film was epitaxially grown along the substrate orientation. Figure 2(b) shows a reciprocal space mapping of the asymmetric (112) peak for the same film. From the peak position, $d_{110} = 3.331$ Å and $d_{002} = 1.599$ Å are extracted, which indicate that the lattice is elastically strained with a compression along the in-plane by ~0.6% with respect to bulk SnO₂ ($d_{110} = 3.350$ Å and $d_{002} = 1.593$ Å). In fact, d_{110} and d_{002} change systematically as shown in the inset of figure 2(b) for all the films, to which a linear fit gives a slope of

0.67 that is close to the value calculated from elastic stiffness constants as $C_{13}/C_{33} = 0.69$ [11]. As for 750°C-annealed and overgrown films, the film lattice was relaxed to $d_{110} = 3.336$ Å and $d_{002} = 1.598$ Å. The 800°C-annealed film is less strained ($d_{110} = 3.340$ Å and $d_{002} = 1.596$ Å), suggesting that annealing at much higher temperature will allow to accommodate a strain-free buffer layer. The compromise with surface roughening effect prohibits further increase of annealing temperature on achievement of the fully relaxed buffer layer.

We further analyzed the reciprocal space mapping data to extract the lateral grain size. The lateral grain size appears inversely proportional to peak spread along the in-plane direction (the [110] direction in this case) [12]. For instance, the film peak seen in figure 2(b) has broader profile along the horizontal axis. By measuring the full width at half maximum, we obtain lateral grain size of 90 \pm 20 nm for the as-grown film, and also 220 \pm 30 nm and 60 \pm 20 nm for the overgrown and 800°C-annealed films, respectively (data not shown). Although the evaluation of grain size is inaccurate given the effect of strain on spread width of the diffraction peak, the improved crystalline quality could be further verified by observing cross-section TEM for as-grown and overgrown SnO₂ films as shown in figure 3. In the TEM image of the as-grown film, there can be seen dense defects near the interface, which extend to the surface. However, most of defects were congregated only in the buffer layers in annealed buffer layer and rather low density of defects extends to the overgrown layer.

Now, we compare the electrical properties of the films. Figure 4 shows the temperature dependence of conductivity (σ) and Hall mobility measured for three representative films discussed so far. $\mu_{\rm H}$ of as-grown film at 300K is 36 cm²V⁻¹s⁻¹, which is comparable to nominally undoped films grown on sapphire substrates [6] whereas the carrier density is as low as 1.0×10^{18} cm⁻³, which is notably lower than that grown on the sapphire substrates by an order of magnitude. The overgrown SnO₂ film has a comparable $\mu_{\rm H}$ at 300K with the as-grown film but increases its value up to 38 cm²V⁻¹s⁻¹ at 200K, representing bell-shaped temperature dependence. The 750°C-annealed film had much lower $\mu_{\rm H}$ and σ

than those of as-grown and overgrown films. Therefore, we conclude that the annealing process degrades the electron mobility but overgrown layer has much better quality than that as-grown film. In fact, a thicker as-grown film (200 nm) indicated essentially no difference in electrical properties with respect to the 100-nm-thick as-grown film, which excludes mere thickness dependence, but confirms the effect of buffer layer. When we compare the results of figures 3 and 4 with the film grown on sapphire substrates [6] we presume that different carrier concentration arises from their own intrinsic defects. Since annealed buffer layer reduces defects extended to the overgrown layer, mobility is enhanced at low temperatures.

4. Conclusions

We have demonstrated the effect of annealed SnO_2 buffer layer on the improved crystalline quality and electrical properties of overgrown SnO_2 films on (001) TiO_2 substrates. We find an optimum annealing condition of the buffer layer to achieve atomically flat surface and partially relaxed lattice, both of which are required to improve structural and electrical properties of overgrown layer. The peak mobility of 38 cm²V⁻¹s⁻¹ is recorded at 200 K for the film grown on the buffer layer annealed at 750°C.

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Figure captions:

Figure 1. RHEED pattern for (a) as-grown, (b) 750°C-annealed, (c) 800°C-annealed, and (d) overgrown SnO_2 films. Corresponding AFM images (2 µm x 2 µm) to (a)~(d) are shown in (e)~(h), respectively.

Figure 2. (a) x-ray diffraction pattern of as-grown film on (001) TiO₂ substrate. (b) Reciprocal space mapping around the asymmetric (112) diffraction peaks for the same sample. The cross stands for the peak position expected for bulk SnO₂. The inset depicts the values of d_{110} and d_{002} for various films described in text.

Figure 3. Cross-section transmission electron microscopy images of as-grown (left) and overgrown SnO₂ (right) films.

Figure 4. Temperature dependence of (a) conductivity σ and (b) Hall mobility $\mu_{\rm H}$ for three representative films. $n_{300\rm K}$ is free electron concentration at 300 K.



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Figure 3. Cross-section transmission electron microscopy images of as-grown (left) and overgrown SnO₂ film (right).

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Figure 4. Temperature dependence of (a) conductivity σ and (b) Hall mobility $\mu_{\rm H}$ for three representative films. $n_{300\rm K}$ is free electron concentration at 300 K.

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