

論文 / 著書情報  
Article / Book Information

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Title(English)	A Study of Fractional-N Oversampling Frequency Synthesizers Using Digital-Assisted Calibration
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# 論文要約

## THESIS OUTLINE

系・コース： <small>Department of, Graduate major in</small>	電気電子 系 コース	申請学位 (専攻分野)： <small>Academic Degree Requested</small>	博士 Doctor of (philosophy)
学生氏名： <small>Student's Name</small>	QIU JUNJUN	指導教員 (主)： <small>Academic Supervisor(main)</small>	岡田健一

This thesis focusses on the study of fractional-N oversampling PLL design with the digital-assisted calibration methods.

1. Introduction
  - 1.1 Internet of Things
  - 1.2 Trends of Integrated Circuits
  - 1.3 Trends of PLL Design
  - 1.4 Thesis Organization
2. Conventional Sampling PLL Design and Limitation
  - 2.1 Fractional-N PLL Introduction
  - 2.2 PLL Design Basics
  - 2.3 Sampling PLL Design
  - 2.4 Limitation of The Conventional PLLs
3. Uniform Oversampling PLL Design
  - 3.1 Introduction
  - 3.2 Circuit Implementation
  - 3.3 Measurement Results
  - 3.4 Conclusion
4. Other Oversampling PLL Design
  - 4.1 Introduction
  - 4.2 Circuit Implementation
  - 4.3 Simulation and Measurement Results
  - 4.4 Conclusion
5. PLL Application in Receiver Baseband Circuit
  - 5.1 Introduction
  - 5.2 Prior Demodulation Method
  - 5.3 Implementation and Simulation
  - 5.4 Performance Evaluation
  - 5.5 Conclusion
6. Conclusion and Future Work
  - 6.1 Conclusion
  - 6.2 Future Directions